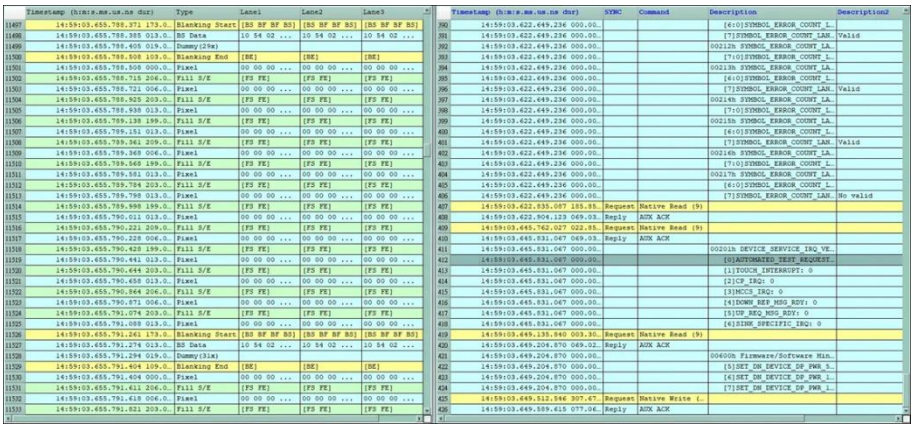
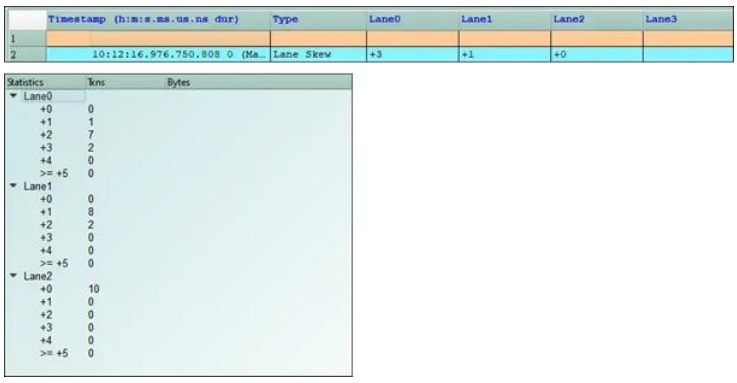
- Option for Protocol Analyzer, when paired with the eDP 1.4a option is capable of eDP1.4a protocol analysis
- **Applicable Model:** BF7264 PRO Bus Finder
- The embedded DisplayPort (eDP) 1.4a enables a higher video data transfer rate for increased panel resolution, greater color depth and higher refresh rates, incorporating VESA Display Stream Compression (DSC) Standard v1.1 and new segmented panel architecture that enables higher panel integration. These and other refinements were made to take advantage of higher GPU video performance and new display technologies, while also enabling reduced system power and form factor
- Primarily used in embedded display applications, such as laptops, tablets and all-in-one computers for connecting internal displays to the graphics output. Key applications include high-resolution displays with high refresh rates (up to 8K), power efficiency, and features like Display Stream Compression (DSC) and Panel Self Refresh (PSR)
- eDP1.4a Way Station support;
  - ✓ eDP 1.4a up to 5.4Gbps, 4 Lanes
  - ✓ Data Filter removes dummy, video data, filling S/E to save memory
  - ✓ Display eDP image data including RGB, YCbCr, format or compressed DSC packets
  - ✓ eDP command statistics include numbers of packets, individual command, different data length and errors
  - ✓ eDP command trigger
- 32Gb RAM as buffer to stream eDP data into SSD HD to record data flow from Low Power to High-Speed Mode

SOFTWARE:

- **Display eDP1.4a protocol packet data in table format, including DP Aux Ch command analysis**



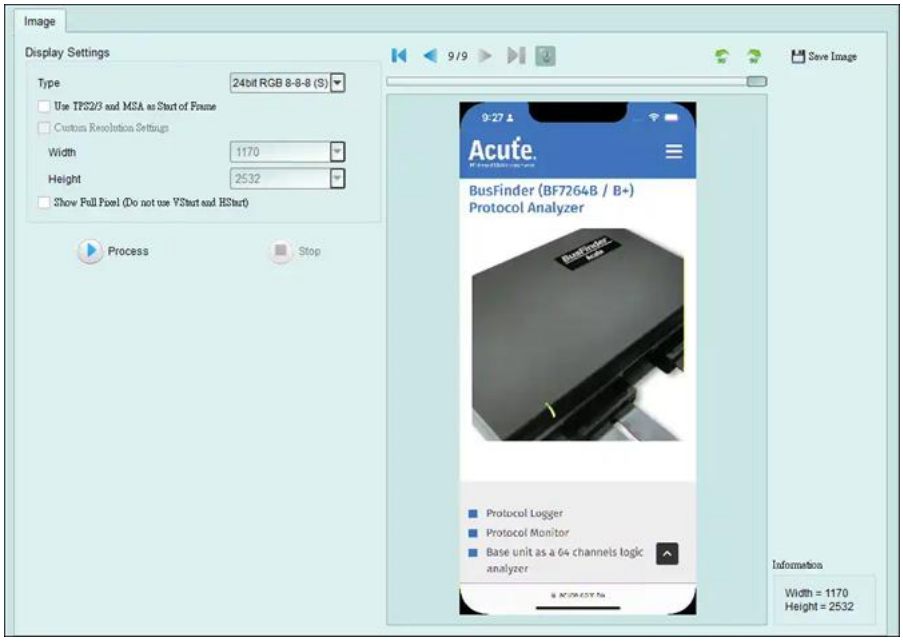
- **eDP Lane Skew display and statistics:**



	Timestamp (h:m:s.ms.us.ns dur)	Type	Lane0	Lane1	Lane2	Lane3
1						
2	10:12:16.976.750.808 0 (Ma..	Lane Skew	+3	+1	+0	

Statistics	Txns	Bytes
Lane0		
+0	0	
+1	1	
+2	7	
+3	2	
+4	0	
>= +5	0	
Lane1		
+0	0	
+1	8	
+2	2	
+3	0	
+4	0	
>= +5	0	
Lane2		
+0	10	
+1	0	
+2	0	
+3	0	
+4	0	
>= +5	0	

- **Display eDP image data including RGB, YCbCr, format or compressed DSC packets:**



- **eDP command trigger, can trigger external oscilloscopes synchronously:**