



Features and Functions:

- Real-time data display, post-capture waveform
- 32GB total memory
- Trigger for commands or data
- Filter data to save more commands
- Two voltage detects to find design bugs from voltage drop
- 64 channels Logic Analyzer
- Different active probes for different protocols for easier connections
- Protocol Option: eDP1.4a, eMMC 5.1, MIPI D-PHY 1.2, NAND Flash, SD 3.0(SDIO 3.0), SD 4.1 (UHS-II), SGMII, UFS2.1
- Different active probes for different protocols for easier connections
- Acts as Protocol logger and Protocol monitoring

It is hardware decoding, may log protocol data very long time if without waveforms. Application timing: Preliminary protocol debug.

Protocol Option:

- eDP1.5 Option: Support eDP1.4a, up to 5.4Gbps/per lane, 4 Lanes
- eMMC Option: Support eMMC 5.1 HS400 / HS200 / CMD Queue
- MIPI D-PHY 1.2 Option: Support D-PHY V1.2, up to 2.0Gbps per lane, 1 + 4 Lanes
- NAND Flash Option: Support ONFI 4.1 (NV-DDR3), Mode 8 / Toggle DDR 2.0~267MHz
- SD 3.0 (SDIO 3.0) Option: Support SD 3.0 SDR104 / SD6.0 Legacy mode SDR104, DDR200, Command Queue/ SDIO 3.0
- SD 4.1 (UHS-II) Option: Support UHS156, up to 1.56Gbps per lane
- SGMII Option: Speed support 1000M, 100M, 10M

Support Logic Analyzer:

- 4GHz timing analysis
- 8-state flow chart bus triggers
- Bus decodes with waveforms
- Stacks with a DSO to form as an MSO




Other Options:

- LA: Support eMMC 5.1, NAND Flash, SD 3.0 (SDIO 3.0), Serial Flash (SPI NAND), SPI
- LVDS: Low-voltage differential signal measurement supporting logic signals

▪ **Protocol Analyzer Mode:**

- ✓ Recording and Monitoring:

During hardware decoding, may log protocol data for very long time if without waveforms.

Protocol Analyzer	Protocol Logger	Protocol Monitor
Show real-time protocol data. Application timing: massive protocol data with some idles in between	Like data logger, save massive data into SSD hard drive. Application timing: massive protocol data	Like dash cameras, record protocol data by the device's memory only. Application timing: trigger event only happens in very long time.
		

▪ **Logic Analyzer Mode:**

- ✓ Capture digital waveforms and support bus decodes. Able to stack with a DSO to form as an MSO
- ✓ Provides multiple storage modes, users could select to have long time recording or precision acquisition



- ✓ Long Time Record: Transitional Storage VS Compressed Storage

For signal capture and analysis, usually require to record the signal for a long time. If data is stored in a compressed manner, it may cause the software to lag or stop functioning due to insufficient PC memory when the data is decompressing after it is sent to the computer. For smooth software operation and long-term recording without any missing data, ACUTE's analyzer adopts the method of transitional storage rather than compression. After data returns to the PC software, it does not need to perform decompression and the decoded results can be displayed upon completion of the analysis.

